Modulation of High-Voltage Multilevel Inverters: A Comprehensive Review of Strategies, Performance, and Future Directions

Ankit[1], Saurabh Gupta[2], Devendra Sharma[3], Vikas Gupta[4]

ankitkhade012@gmail.com[1], saurabhgupta.sgsits@gmail.com[2],

devendrasharma798@gmail.com[3],directortit@gmail.com[4]

[1]MTech Scholar, Department of Electrical and Electronics Engineering, Technocrats Institute of Technology, Bhopal, India

[2]Professor, Department of Electrical & Electronics Engineering, Technocrats Institute of Technology, Bhopal, India

[3]Assistant Professor, Department of Electrical & Electronics Engineering, Technocrats Institute of Technology, Bhopal, India

[4]Assistant Professor, Department of Electrical & Electronics Engineering, Technocrats Institute of Technology, Bhopal, India

Abstract— High-voltage multilevel inverters (MLIs) have become a cornerstone of modern power conversion systems, enabling efficient and high-quality power delivery for applications ranging from renewable energy integration to industrial motor drives and flexible AC transmission systems. The performance of an MLI is critically dependent on the modulation technique employed to synthesize a high-quality output voltage from multiple DC sources. This paper presents a comprehensive review of the primary modulation strategies developed and analyzed for high-voltage multilevel inverters. It systematically classifies these techniques based on their switching frequency and operational principles, including fundamental frequency switching methods like selective harmonic elimination and space vector control, and high-frequency carrier-based methods such as sinusoidal pulse-width modulation and space vector modulation. The paper delves into the design considerations, operational analysis, comparative performance in terms of total harmonic distortion and switching losses, and the specific advantages and disadvantages of each technique. Furthermore, it explores emerging trends in advanced modulation schemes, including model predictive control and techniques tailored for new converter topologies. The objective is to provide a consolidated reference for researchers and engineers, elucidating the

evolution, current state, and future prospects of modulation in the field of high-voltage multilevel inverters.

Keywords—Multilevel Inverter, Modulation Techniques, Selective Harmonic Elimination (SHE), Sinusoidal Pulse Width Modulation (SPWM), Space Vector Modulation (SVM), Total Harmonic Distortion (THD), High-Voltage Applications.

1. INTRODUCTION

The demand for high-power, medium-voltage energy conversion systems has grown exponentially with the expansion of industrial automation, electric grids, and renewable energy sources. Traditional two-level inverters, while simple in structure and control, face significant limitations in high-voltage applications. They require high-voltage-rated switches, produce high dv/dt stress on loads and machines, and generate an output voltage rich in harmonics, necessitating large and expensive passive filters.

To overcome these challenges, multilevel inverters (MLIs) emerged as a superior alternative [1]. By synthesizing a staircase-like output voltage waveform from several lower-voltage DC sources, MLIs offer numerous advantages. These include the ability to operate at higher voltages using devices with lower voltage ratings, reduced harmonic distortion in the output voltage and current, lower electromagnetic interference (EMI), and a significant reduction in the filter size requirements [2].

The heart of an MLI's operation lies in its modulation strategy. The modulation controller is responsible for determining the switching instants of the power semiconductor devices to reconstruct a desired reference waveform, typically sinusoidal, from the available discrete voltage levels. The choice of modulation technique has a profound impact on several key performance indicators of the inverter: the quality of the output waveform measured by total harmonic distortion (THD), the magnitude of switching and conduction losses, which affect the overall efficiency, the complexity of the digital implementation, and the dynamic response of the system [3].

This review paper provides a structured overview of the major modulation techniques developed for high-voltage multilevel inverters. It aims to guide the reader through the fundamental principles, design considerations, and comparative performance of these methods, highlighting their suitability for different applications and topologies.

2. CLASSIFICATION OF MODULATION TECHNIQUES

Modulation techniques for MLIs can be broadly classified based on the frequency at which the power switches operate. The two primary categories are fundamental switching frequency techniques and high switching frequency techniques. A further classification can be based on the modulation principles, such as carrier-based or space-vector-based methods.

3. FUNDAMENTAL SWITCHING FREQUENCY TECHNIQUES

In these methods, each power switch typically conducts for either one or a few cycles of the fundamental output frequency. This results in very low switching losses, making them highly attractive for very high-power applications where efficiency is paramount and heat management is a significant challenge.

A. Selective Harmonic Elimination (SHE)

Selective Harmonic Elimination, also known as fundamental frequency switching or programmed PWM, is a prominent pre-calculated modulation technique [4]. The principle of SHE involves determining the specific switching angles within a quarter cycle of the output voltage waveform to eliminate specific low-order harmonics while controlling the magnitude of the fundamental component.

For an MLI with 's' number of DC sources (for a cascaded H-bridge) or voltage levels, a set of nonlinear transcendental equations is formulated. For example, for a waveform with N switching angles per quarter cycle, one equation is used to set the desired fundamental amplitude, and the remaining (N-1) equations are set to zero to eliminate N-1 specific low-order harmonics (e.g., 5th, 7th, 11th, 13th).

The primary challenge with SHE is the computational complexity involved in solving these equations offline. Numerous numerical techniques, such as the Newton-Raphson method, and more recently, artificial intelligence-based optimization algorithms like Genetic Algorithms (GA)

and Particle Swarm Optimization (PSO), have been employed to compute the switching angles for a wide range of modulation indices [5]. The key advantage of SHE is its ability to produce a high-quality output voltage with a very low switching frequency, leading to minimal switching losses. However, its dynamic performance is limited, as the pre-calculated angles are optimized for steady-state operation.

B. Space Vector Control (SVC)

Space Vector Control, often referred to as the fundamental frequency space vector modulation, is another low-switching frequency technique. In this method, the inverter only utilizes the large, active voltage vectors that are available in the space vector diagram of the multilevel inverter. It does not use the zero vectors or any form of pulse width modulation [6]. The output voltage is synthesized by sequencing through these discrete voltage vectors at the fundamental frequency. While the control is simple, the output voltage quality is generally poorer compared to SHE or high-frequency PWM methods, as it offers no direct control over harmonic content.

4. HIGH SWITCHING FREQUENCY TECHNIQUES

High switching frequency techniques, generally operating in the range of a few hundred Hz to several kHz, utilize the concept of averaging. By switching the devices rapidly, an average voltage value, equal to the desired reference voltage over a switching period, is synthesized. These methods are known for their excellent dynamic response and high-quality output waveforms.

A. Carrier-Based Pulse Width Modulation (CB-PWM)

Carrier-based PWM is an extension of the well-known bipolar and unipolar PWM techniques used in two-level inverters. The core principle involves comparing a sinusoidal modulating or reference wave (at the desired fundamental frequency, fref) with several high-frequency triangular carrier waves (at the switching frequency, f_sw) to generate the switching pulses for the devices [7]. For an m-level inverter, (m-1) carrier waves with the same amplitude and frequency are required. These carriers are vertically disposed to occupy contiguous bands. The major variants of CB-PWM are defined by the phase relationship between these carriers:

Phase Disposition (PD-PWM): In this most common method, all carriers are in phase with each other. This technique is widely used because it results in the best harmonic performance for most

MLI topologies, with the harmonics concentrated at the carrier frequency, leading to the lowest line-to-line voltage THD [8].

Phase Opposition Disposition (POD-PWM): In POD, the carriers above the zero reference point are 180 degrees out of phase with those below the zero reference. This causes large harmonic components at the carrier frequency but cancels them out in the line-to-line voltage.

Alternative Phase Opposition Disposition (APOD-PWM): Here, every adjacent carrier band is phase-shifted by 180 degrees from its neighbor. This distributes the harmonic energy sidebands more evenly.

Phase-Shifted PWM (PS-PWM): Primarily used for cascaded H-bridge and modular multilevel converters, PS-PWM uses a single carrier wave but applies it to each module with a specific phase shift. For an inverter with N series-connected modules, the carriers for each module are phase-shifted by 180°/N (for unipolar modulation). This technique results in a highly effective switching frequency seen at the output that is N times the actual switching frequency, effectively pushing the first set of harmonics to a much higher frequency band and drastically reducing the output filter requirement [9].

The design of CB-PWM involves selecting the appropriate disposition method, carrier frequency, and modulation index. The analysis primarily focuses on the harmonic spectrum and the linearity of the fundamental voltage control.

B. Space Vector Modulation (SVM)

Space Vector Modulation is a digital control technique that approaches the modulation problem from a complex plane (dq-frame) perspective [10]. Instead of dealing with individual phase voltages, it treats the three-phase quantities as a single rotating voltage vector. For a multilevel inverter, the space vector diagram consists of a hexagon subdivided into numerous triangles. The vertices of these triangles represent the discrete switching states (voltage vectors) of the inverter.

The principle of SVM is to approximate the reference voltage vector (Vref) by time-averaging the three nearest voltage vectors that form the vertices of the triangle in which Vref lies. The algorithm for multilevel SVM involves three main steps:

Identification of the Sector and Triangle: Determine the location of the reference vector within the space vector diagram.

Calculation of Dwell Times (T1, T2, T3): Using the "volt-second balancing" principle, calculate the time duration for which each of the three selected vectors must be applied over one switching period (Ts). This is typically done using algebraic equations based on the coordinates of the vectors.

Determination of the Switching Sequence: Arrange the switching states of the selected vectors in an optimal sequence, typically to minimize the number of device switchings per cycle (e.g., using a seven-segment or five-segment sequence).

The primary challenge in implementing SVM for higher-level inverters (levels > 3) is the complexity of the calculations required to identify the triangle and compute the dwell times. To manage this complexity, techniques such as coordinate transformation (e.g., from Cartesian to gh coordinates) and look-up tables are often employed. SVM is highly regarded for its superior DC bus utilization (typically 15% higher than CB-PWM), lower THD, and suitability for digital implementation, especially in closed-loop control systems like field-oriented control for motor drives [11].

5. COMPARATIVE ANALYSIS AND DESIGN CONSIDERATIONS

The selection of a modulation technique for a high-voltage MLI involves a trade-off study based on several key parameters:

Total Harmonic Distortion (THD): High-frequency PWM methods (SVM, PD-PWM) generally produce the lowest THD, especially with a higher number of levels. Among fundamental frequency methods, SHE can achieve very low THD at the designed operating point but may degrade elsewhere.

Switching Losses: Fundamental frequency methods (SHE, SVC) offer the lowest switching losses, making them ideal for multi-megawatt applications. High-frequency methods incur higher losses, but techniques like PS-PWM can distribute these losses evenly among numerous modules.

Dynamic Response: High-frequency PWM techniques have a much faster dynamic response as the control loop can be updated every switching cycle. This is crucial for applications like traction and renewable energy grid integration. Fundamental frequency methods are inherently slower.

Computational Complexity: CB-PWM techniques, particularly PD-PWM, are straightforward to implement using low-cost digital signal processors (DSPs) or field-programmable gate arrays (FPGAs). SVM and SHE require significantly more computational resources, especially as the number of levels increases.

DC Bus Utilization: SVM inherently provides the highest utilization of the DC bus voltage, allowing for a higher output voltage for the same DC link.

6. EMERGING TRENDS AND ADVANCED MODULATION SCHEMES

Recent research in MLI modulation is driven by the advent of new topologies and the need for enhanced performance under non-ideal conditions.

Model Predictive Control (MPC): MPC has gained significant attention for power converter control [12]. In this method, a model of the inverter and load is used to predict the future behavior of system variables (e.g., load current) for all possible switching states. A cost function, which can include terms for current tracking error, switching frequency, and even capacitor voltage balancing, is then evaluated to select the optimal switching state to be applied in the next sampling interval. MPC offers excellent dynamic response and flexibility to handle multiple control objectives simultaneously. However, its main challenges are the high computational burden, especially for multilevel topologies, and the variable switching frequency, which can complicate filter design.

Hybrid Modulation Techniques: Researchers are increasingly combining the strengths of different methods. For example, a hybrid approach might use SHE in steady-state for high efficiency and switch to SVM or CB-PWM during transient conditions for fast dynamic response.

Modulation for New Topologies: The rise of the Modular Multilevel Converter (MMC) has spurred the development of specialized modulation techniques. Capacitor voltage balancing in the numerous submodules of an MMC is a critical task that is often integrated into the modulation

strategy, with sorting algorithms being a key component of techniques like PS-PWM and Nearest Level Modulation (NLM) for MMCs [13].

Asymmetrical Source Utilization: Modulation techniques are being designed to optimally utilize asymmetrical DC sources (where DC link voltages are not equal) to increase the number of output levels without adding hardware, thereby improving power quality [14].

7. CONCLUSION

The design and analysis of modulation techniques are central to the successful deployment of high-voltage multilevel inverters. This review has traversed the landscape from fundamental switching methods, which prioritize efficiency, to high-frequency methods, which excel in waveform quality and dynamic control. Selective Harmonic Elimination remains a powerful tool for very high-power applications where minimizing losses is critical. Carrier-based PWM, particularly its various disposition and phase-shifted variants, offers a simple and effective solution for a wide range of applications. Space Vector Modulation, despite its complexity, provides superior performance in terms of DC bus utilization and harmonic quality, making it a staple in high-performance drives.

The choice is not absolute but depends entirely on the application's specific demands. As power systems evolve, future research will continue to refine these techniques, with advanced methods like Model Predictive Control and hybrid strategies paving the way for more intelligent, efficient, and robust power converters. The ongoing development of new topologies will also necessitate a parallel evolution in their modulation and control, ensuring that multilevel inverters remain at the forefront of high-voltage power conversion technology.

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